L Number	Hits	Search Text	DB	Time stamp
-	19	(motherboard and computer and (cpu microprocessor processor) and dsp and memory) and ((spoken voice speech) adj1 command)	USPAT; US-PGPUB; EPO; JPO; DERWENT;	2003/04/17 21:20
-	33	(motherboard and computer and (cpu microprocessor processor) and dsp and memory) and ((spoken voice speech audio) adj1 command)	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/04/17 21:20
-	16	(motherboard and computer and (cpu microprocessor processor) and dsp and memory) and ((spoken voice speech audio) adj1 command) and pci	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/17 21:21
-	13	(motherboard and computer and (cpu microprocessor processor) and dsp and memory) and ((spoken voice speech audio) adj1 command) and pci and bus and power and input	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/17 21:24
-	3	(motherboard and computer and (cpu microprocessor processor) and dsp and memory) and ((spoken voice speech) adj1 command) and pci and bus and power and input	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/17 21:24
-	38	(motherboard PCB) and computer and (cpu microprocessor processor) and dsp and memory and ((spoken speech verbal voice audio) adj1 command)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/17 21:56
-	11	((motherboard PCB) and computer and (cpu microprocessor processor) and dsp and memory and ((spoken speech verbal voice audio) adj1 command)) and 704/\$.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/17 22:00
-	. 10	((motherboard PCB) and computer and (cpu microprocessor processor) and dsp and memory and ((spoken speech verbal voice audio) adj1 command)) and 704/\$.ccls. and bus	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/04/17 21:54
-	13	((motherboard PCB) and computer and (cpu microprocessor processor) and dsp and memory and ((spoken speech verbal voice audio) adj1 command)) and pci and power	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/17 21:52
-	47	(motherboard PCB (main adj1 circuit adj1 board) backplane) and computer and (cpu microprocessor processor) and dsp and memory and ((spoken speech verbal voice audio) adj1 command)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/04/17 21:58
-	50	((motherboard mother-board (mother board) PCB (main adj1 circuit adj1 board) backplane) and computer and (cpu microprocessor processor) and dsp and memory and ((spoken speech verbal voice audio) adj1 command)) and 704/\$.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/18 16:02
-	1	((motherboard mother-board (mother board) PCB (main adj1 circuit adj1 board) backplane) and computer and (cpu microprocessor processor) and dsp and memory and ((spoken speech verbal voice audio) adj1 command)) and 704/\$.ccls. and pci	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/04/17 22:02
-	24	((motherboard mother-board (mother board) PCB (main adj1 circuit adj1 board) backplane) and computer and (cpu microprocessor processor) and dsp and memory and ((spoken speech verbal voice audio) adj1 command)) and pci and (memory same dsp)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/17 22:10

-	20	((motherboard mother-board (mother board) PCB (main adj1	USPAT;	2003/04/18 20:54
		circuit adj1 board) backplane) and computer and (cpu	US-PGPUB;	
		microprocessor processor) and dsp and memory and	EPO; JPO;	
		((spoken speech verbal voice audio) adj1 command)) and pci	DERWENT;	
		and (memory same dsp) and power	IBM_TDB	
-	32	(((motherboard mother-board (mother board) PCB (main adj1	USPAT;	2003/04/17 22:11
İ		circuit adj1 board) backplane) and computer and (cpu	US-PGPUB;	
		microprocessor processor) and dsp and memory and	EPO; JPO;	
		((spoken speech verbal voice audio) adj1 command)) and	DERWENT;	
		704/\$.ccls.) and (memory same dsp)	IBM_TDB	
-	23	(((motherboard mother-board (mother board) PCB (main adj1	USPAT;	2003/04/17 22:19
		circuit adj1 board) backplane) and computer and (cpu	US-PGPUB;	
		microprocessor processor) and dsp and memory and	EPO; JPO;	
		((spoken speech verbal voice audio) adj1 command)) and	DERWENT;	
		704/\$.ccls.) and bus and (memory same dsp) and power	IBM TDB	
_	1	"05844824"	USPAT;	2003/04/17 22:19
	,	00011021	US-PGPUB;	2000/04/17 22:10
			EPO; JPO;	
			DERWENT:	
1_	24	(((motherboard mother-board (mother board) PCB (main adj1	IBM_TDB USPAT;	2003/04/17 22:21
-	-4	circuit adj1 board) backplane) and computer and (cpu	US-PGPUB;	2003/04/1/ 22.21
		microprocessor processor) and dsp and memory and	EPO; JPO;	
		((spoken speech verbal voice audio) adj1 command)) and	DERWENT;	
	50	704/\$.ccls.) and bus and (memory same dsp)	IBM_TDB	2002/04/40 40.44
-	50	((motherboard mother-board (mother board) PCB (main adj1	USPAT;	2003/04/18 10:41
		circuit adj1 board) backplane) and computer and (cpu	US-PGPUB;	
		microprocessor processor) and dsp and memory and	EPO; JPO;	
		((spoken speech verbal voice audio) adj1 command) and (bus	DERWENT;	
		pci)) and pci	IBM_TDB	0000/04/40 40 40
-	34	((motherboard mother-board (mother board) PCB (main adj1	USPAT;	2003/04/18 10:42
		circuit adj1 board) backplane) and computer and (cpu	US-PGPUB;	
		microprocessor processor) and dsp and memory and	EPO; JPO;	
		((spoken speech verbal voice audio) adj1 command) and (bus	DERWENT;	1
		pci)) and pci and speech	IBM_TDB	
-	29	((motherboard mother-board (mother board) PCB (main adj1	USPAT;	2003/04/18 16:06
		circuit adj1 board) backplane) and computer and (cpu	US-PGPUB;	
		microprocessor processor) and dsp and memory and	EPO; JPO;	
		((spoken speech verbal voice audio) adj1 command) and (bus	DERWENT;	
		pci)) and pci and (speech adj1 recognit\$4)	IBM_TDB	
-	20	((motherboard mother-board (mother board) PCB (main adj1	USPAT;	2003/04/18 15:57
		circuit adj1 board) backplane) and computer and (cpu	US-PGPUB;	
		microprocessor processor) and dsp and memory and	EPO; JPO;	
		((spoken speech verbal voice audio) adj1 command) and (bus	DERWENT;	
		pci)) and pci and (speech adj1 recognit\$4) and power	IBM_TDB	
-	19	(((motherboard mother-board (mother board) PCB (main adj1	USPAT;	2003/04/18 16:05
		circuit adj1 board) backplane) and computer and (cpu	US-PGPUB;	
		microprocessor processor) and dsp and memory and	EPO; JPO;	
		((spoken speech verbal voice audio) adj1 command) and (bus	DERWENT;	
		pci)) and pci and (speech adj1 recognit\$4) and power) and	IBM_TDB	
		dynamic\$5 and command		
-	1	(((motherboard mother-board (mother board) PCB (main adj1	USPAT;	2003/04/18 16:00
		circuit adj1 board) backplane) and computer and (cpu	US-PGPUB;	
		microprocessor processor) and dsp and memory and	EPO; JPO;	
		((spoken speech verbal voice audio) adj1 command) and (bus	DERWENT;	
		pci)) and pci and (speech adj1 recognit\$4) and power) and	IBM_TDB	
		dynamic\$5 and command and phoneme		
-	50	((motherboard mother-board (mother board) PCB (main adj1	USPAT;	2003/04/18 16:15
		circuit adj1 board) backplane) and computer and (cpu	US-PGPUB;	
		microprocessor processor) and dsp and memory and	EPO; JPO;	
		((spoken speech verbal voice audio) adj1 command)) and	DERWENT;	
		704/\$.ccls.	IBM_TDB	

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-	42	(((motherboard mother-board (mother board) PCB (main adj1	USPAT;	2003/04/18 16:17
		circuit adj1 board) backplane) and computer and (cpu	US-PGPUB;	
		microprocessor processor) and dsp and memory and	EPO; JPO;	
		((spoken speech verbal voice audio) adj1 command)) and	DERWENT;	
		704/\$.ccls.) and vocabulary and ((speech voice) adj1 recognition)	IBM_TDB	
	29	((motherboard mother-board (mother board) PCB (main adj1	USPAT;	2003/04/18 16:15
-	29	circuit adj1 board) backplane) and computer and (cpu	US-PGPUB;	2003/04/16 10.15
		microprocessor processor) and dsp and memory and	EPO; JPO;	
		((spoken speech verbal voice audio) adj1 command) and (bus	DERWENT;	
	:	pci)) and pci and (speech adj1 recogni\$4)	IBM TDB	
_	32	(((motherboard mother-board (mother board) PCB (main adj1	USPAT;	2003/04/18 16:18
-	32	circuit adj1 board) backplane) and computer and (cpu	US-PGPUB;	2003/04/10 10.10
		microprocessor processor) and dsp and memory and	EPO; JPO;	
		((spoken speech verbal voice audio) adj1 command)) and	DERWENT;	
		704/\$.ccls.) and vocabulary and ((speech voice) adj1	IBM_TDB	
		recognition) and bus	IDIVI_IDD	
_	11	(((motherboard mother-board (mother board) PCB (main adj1	USPAT;	2003/04/18 18:19
		circuit adj1 board) backplane) and computer and (cpu	US-PGPUB;	2000/04/10 10:13
		microprocessor processor) and dsp and memory and	EPO; JPO;	
		((spoken speech verbal voice audio) adj1 command)) and	DERWENT;	
		704/\$.ccls.) and vocabulary and ((speech voice) adj1	IBM TDB	
		recognition) and bus and phoneme and (continuous adj1		
		speech) and (voice adj1 command)		
-	20	(((motherboard mother-board (mother board) PCB (main adj1	USPAT;	2003/04/18 20:50
		circuit adj1 board) backplane) and computer and (cpu	US-PGPUB;	
		microprocessor processor) and dsp and memory and	EPO; JPO;	
		((spoken speech verbal voice audio) adj1 command)) and	DERWENT;	
		704/\$.ccls.) and vocabulary and ((speech voice) adj1	IBM TDB	
1.13		recognition) and bus and phoneme		
-	6	"06154830" "06085314"	USPAT;	2003/04/18 18:24
			US-PGPUB;	
			EPO; JPO;	İ
			DERWENT;	
			IBM_TDB	
-	2	"06170049"	USPAT;	2003/04/18 18:24
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	20	(((motherboard mother-board (mother board) PCB) and	USPAT;	2003/04/18 20:53
		computer and (cpu microprocessor processor) and dsp and	US-PGPUB;	
		memory and ((spoken speech verbal voice audio) adj1	EPO; JPO;	
		command)) and 704/\$.ccls.) and vocabulary and ((speech	DERWENT;	
		voice) adj1 recognition) and bus and phoneme	IBM_TDB	
-	10	(((motherboard mother-board) and computer and (cpu	USPAT;	2003/04/18 20:51
		microprocessor processor) and dsp and memory and	US-PGPUB;	
		((spoken speech verbal voice audio) adj1 command)) and	EPO; JPO;	
		704/\$.ccls.) and vocabulary and ((speech voice) adj1	DERWENT;	
		recognition) and bus and phoneme	IBM_TDB	
-	20	((((mother board) PCB) and computer and (cpu	USPAT;	2003/04/18 20:53
		microprocessor processor) and dsp and memory and	US-PGPUB;	
		((spoken speech verbal voice audio) adj1 command)) and	EPO; JPO;	
		704/\$.ccls.) and vocabulary and ((speech voice) adj1	DERWENT;	
		recognition) and bus and phoneme	IBM_TDB	
-	11	((motherboard mother-board (mother adj1 board) PCB (main	USPAT;	2003/04/18 20:55
		adj1 circuit adj1 board) backplane) and computer and (cpu	US-PGPUB;	
		microprocessor processor) and dsp and memory and	EPO; JPO;	
		((spoken speech verbal voice audio) adj1 command)) and pci	DERWENT;	
		and (memory same dsp) and power	IBM_TDB	
-	87	PCI and PDA and dsp and cpu	USPAT;	2003/04/18 21:28
			US-PGPUB	
-	36	PCI and PDA and dsp and cpu and ((speech voice) adj1	USPAT;	2003/04/18 22:42
	•_	recognition)	US-PGPUB	
-	9	PCI and ((cellular mobile) adj1 phone) and (speech adj1	USPAT;	2003/04/18 22:55
		recognition) and dsp	US-PGPUB	1

-	3	PCI and ((cellular mobile) adj1 phone) and vocabulary	USPAT;	2003/04/18 22:56
			US-PGPUB	
-	46	704/\$.ccls. and ((mobile (phone telephone)) or (cellular	USPAT;	2003/04/19 20:11
		phone)) and DSP and (noise adj1 cancel\$6)	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	2000/04/40 04 00
-	4	(dsp (dsp adj1 chip)) and (menu near1 select\$) and (voice	USPAT;	2003/04/19 21:03
	7	adj1 dialing)	US-PGPUB	0000/04/40 04 05
-	/	(dsp (dsp adj1 chip)) and (menu near1 select\$) and (voice	USPAT;	2003/04/19 21:05
		adj1 dialing)	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
	0	(many poort coloots) and (voice adit dialing) and (noice adit	IBM_TDB	2002/04/40 24:20
-	0	(menu near1 select\$) and (voice adj1 dialing) and (noise adj1	USPAT;	2003/04/19 21:29
		cancellation)	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
	5	(voice adj1 dialing) and (dsp (dsp adj1 chip)) and (menu adj1	IBM_TDB	2003/04/19 22:54
-	5	(voice adji dialing) and (dsp (dsp adji chip)) and (mend adji select\$)	USPAT;	2003/04/19 22:54
		Selecty)	US-PGPUB; EPO; JPO;	
			DERWENT;	
			IBM_TDB	
_	5	(noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (menu	USPAT;	2003/04/19 23:05
_		adj1 select\$)	US-PGPUB;	2003/04/19 23:03
		adji dolodity	EPO; JPO;	
			DERWENT;	
			IBM_TDB	
_	13	(noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (menu)	USPAT;	2003/04/19 23:10
		(noise day) same of and (dep (dep day) simply and (mond)	US-PGPUB;	2000/01/10/20:10
			EPO; JPO;	
			DERWENT;	
			IBM TDB	
-	20	(noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (speech	USPAT;	2003/04/19 23:14
		adj1 recognition)	US-PGPUB	
		, ,	EPO; JPO;	1
			DERWENT;	1
			IBM_TDB	
-	5	((noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (speech	USPAT;	2003/04/19 23:13
		adj1 recognition)) and (dsp same (speech adj1 recognition))	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	1	(noise adj1 cancel\$5) same (dsp) and (speech adj1	USPAT;	2003/04/19 23:14
		recognition)	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	